





ATTY DOCKET NO. <b>BU998050DIV1</b>		SERIAL NO. <b>09/805,200</b>
APPLICANT(S) <b>William P. Moore, et al.</b>		
FILING DATE <b>March 14, 2001</b>		GROUP <b>2181</b>

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SG	5,867,644	02/02/99	Ranson, et al.			
SG	6,237,101 B1	05/22/01	Moore, et al.			
SG	5,862,148	01/19/99	Typaldos, et al.			
SG	2002/0013912 A1	01/31/02	Lin			
SG	5,732,255	03/24/98	Verbauwhede			
SG	5,276,824	01/04/94	Skruhak, et al.			
SG	5,790,874	08/04/98	Takano, et al.			

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**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

SG			J. Patrick Brennan, et al. "Low Power Methodology and Design Techniques for Processor Design", 1998, Pages 268-273.

EXAMINER <i>Shane Derratt</i>	DATE CONSIDERED <i>4/6/04</i>
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.